

## Communication Node Receipt of Node-Output Information From Processorless Central Equipment

FEB 28 2006

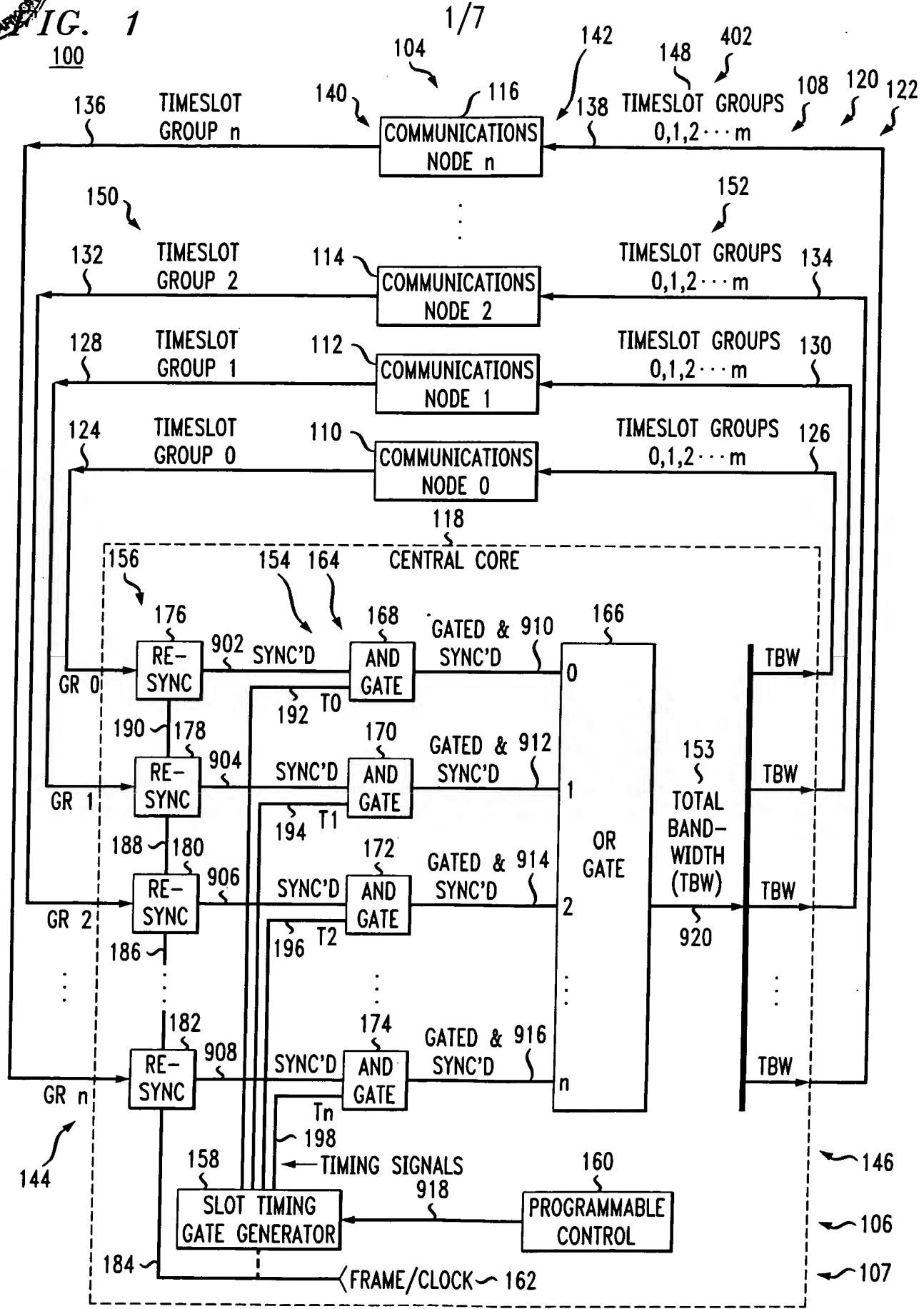


FEB 28 2006

AME 1412

Serial No.: 09/945.558

## REPLACEMENT SHEETS



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**FIG. 2**

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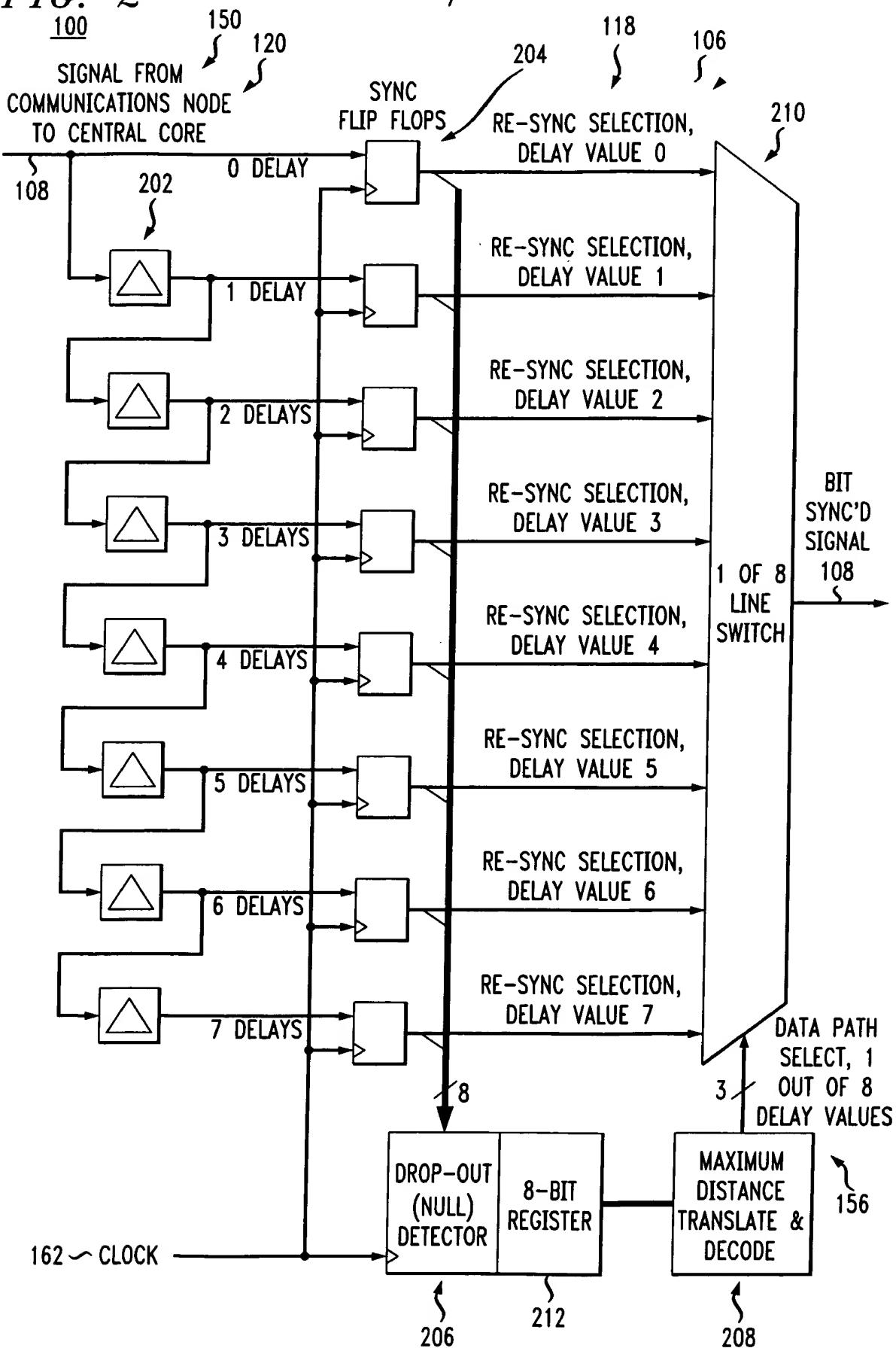
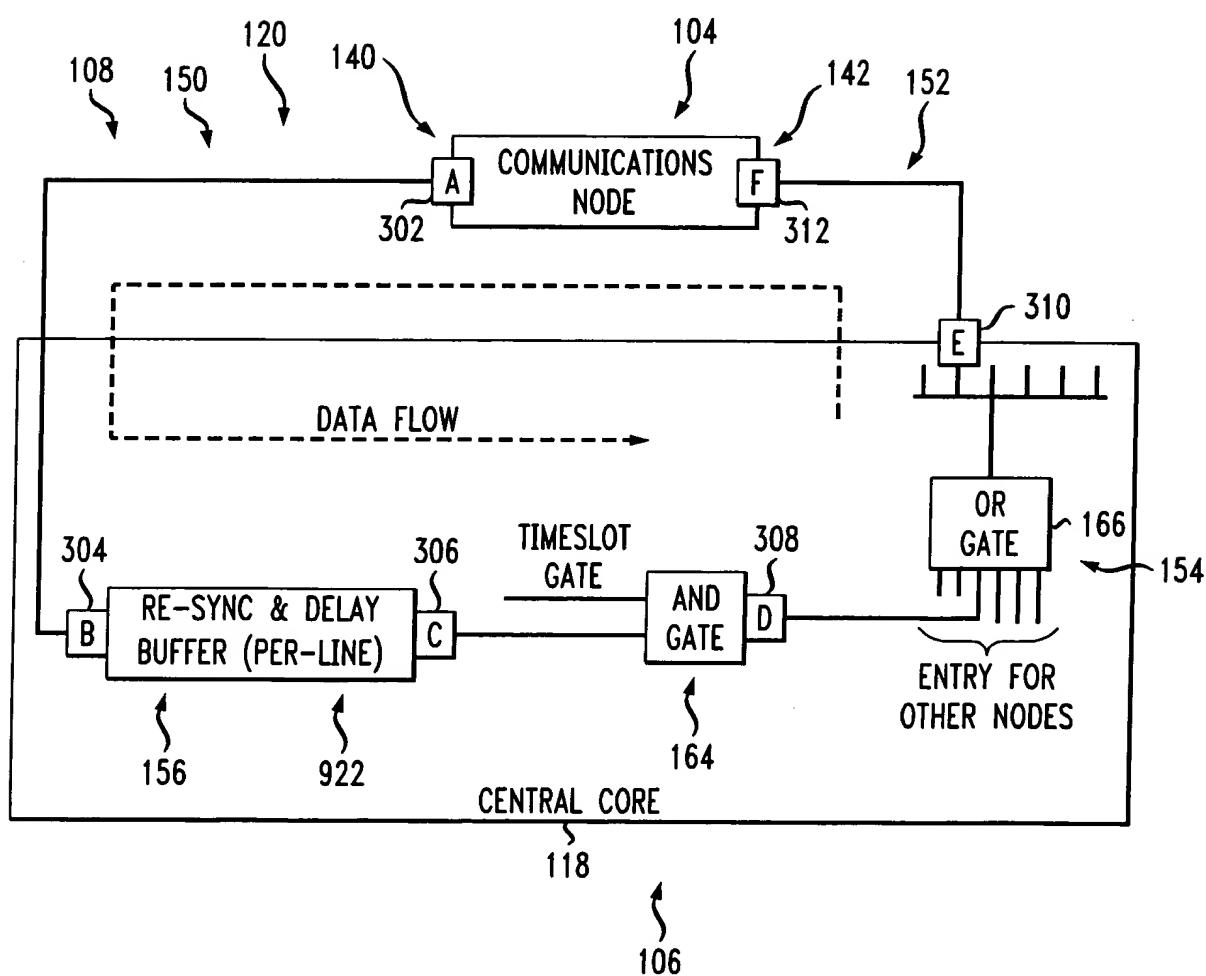


FIG. 3

100

*FIG. 4*

100

BEGINNING OF  
CENTRAL CORE ~ 404

402

302 — A. NODE INITIALLY BROADCASTS UNIQUE DATA IN ALL TIMESLOTS. FRAME IS TRANSMITTED  $1t$  BEFORE CENTRAL CORE FRAME STARTS ( $2t$  BEFORE THE NEXT CENTRAL CORE FRAME IS RECEIVED).

304 — B. DATA IS RECEIVED BY CENTRAL CORE DELAY BUFFER LESS THAN 1.23-USEC BEFORE THE START OF THE NEXT CENTRAL CORE FRAME.

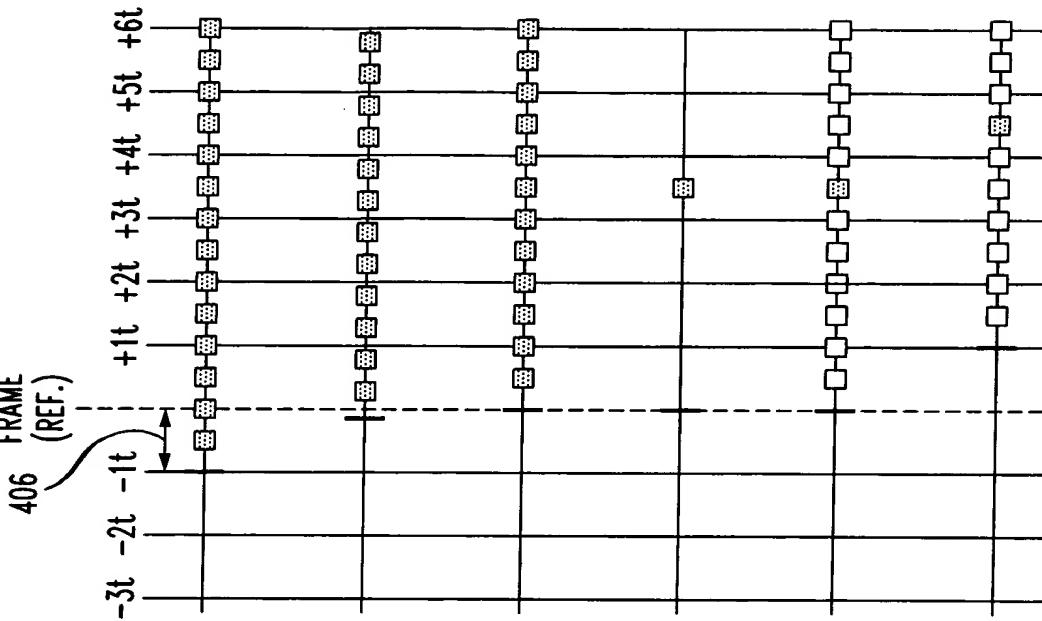
306 — C. DELAY BUFFER ADJUSTS DELAY TO ALIGN FRAME MARK WITH CENTRAL CORE FRAME.

308 — D. AND GATE ONLY Allows DATA IN TIMESLOT(S) ASSIGNED TO SOURCE NODE TO PASS THROUGH TO OR GATE.

310 — E. OR GATE COMBINES DATA FROM ALL NODES TO FORM SYSTEM BACKBONE SIGNAL.

312 — F. SOURCE NODE READS ALL TIMESLOTS TO LOCATE THE UNIQUE DATA IT TRANSMITTED TO DETERMINE IT'S ASSIGNED TIMESLOT(S).

$-3t$     $-2t$     $-1t$     $+1t$     $+2t$     $+3t$     $+4t$     $+5t$     $+6t$

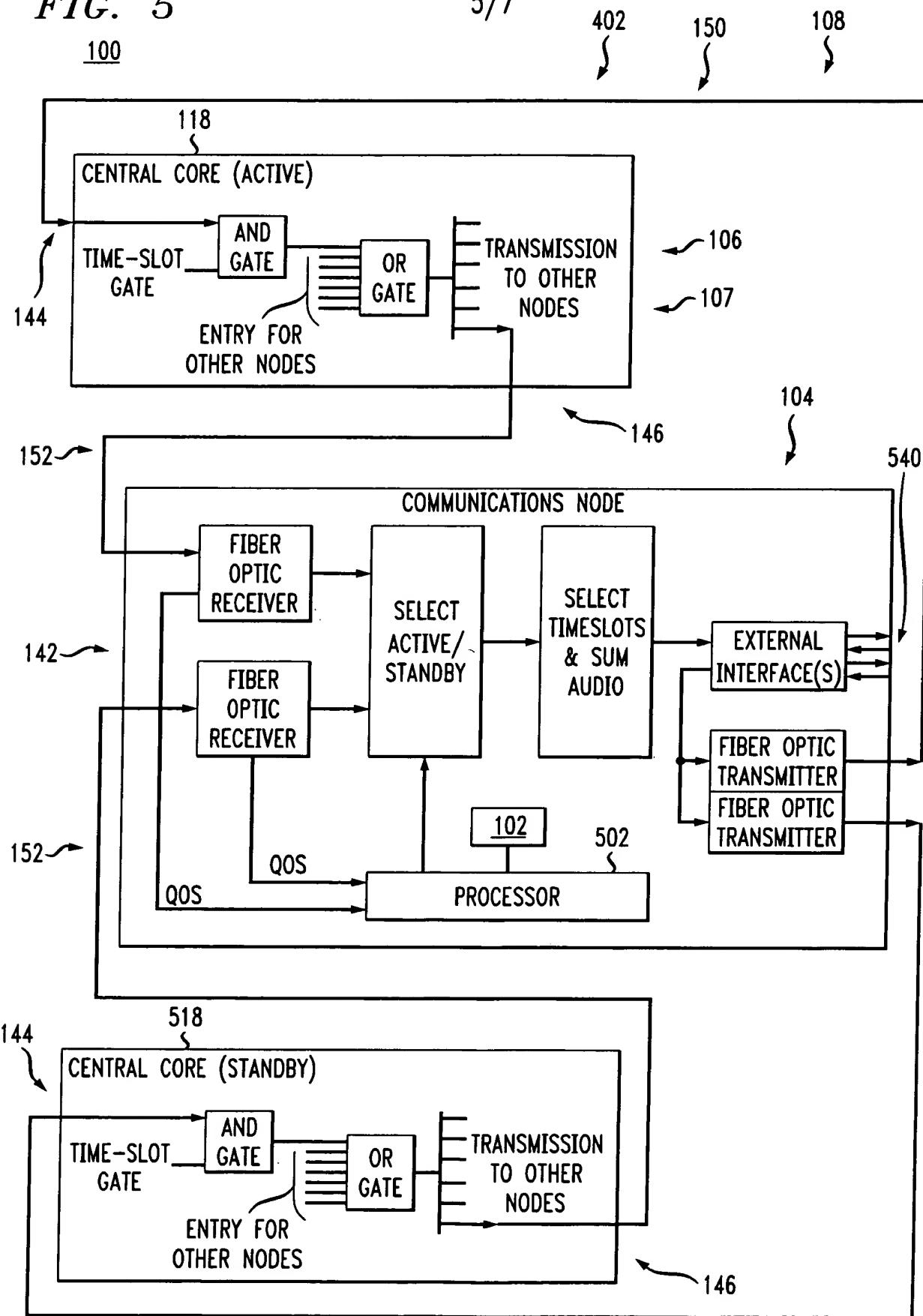


$t$  = MAXIMUM SIGNAL PROPAGATION DELAY FROM CENTRAL CORE TO COMMUNICATIONS NODE

**FIG. 5**

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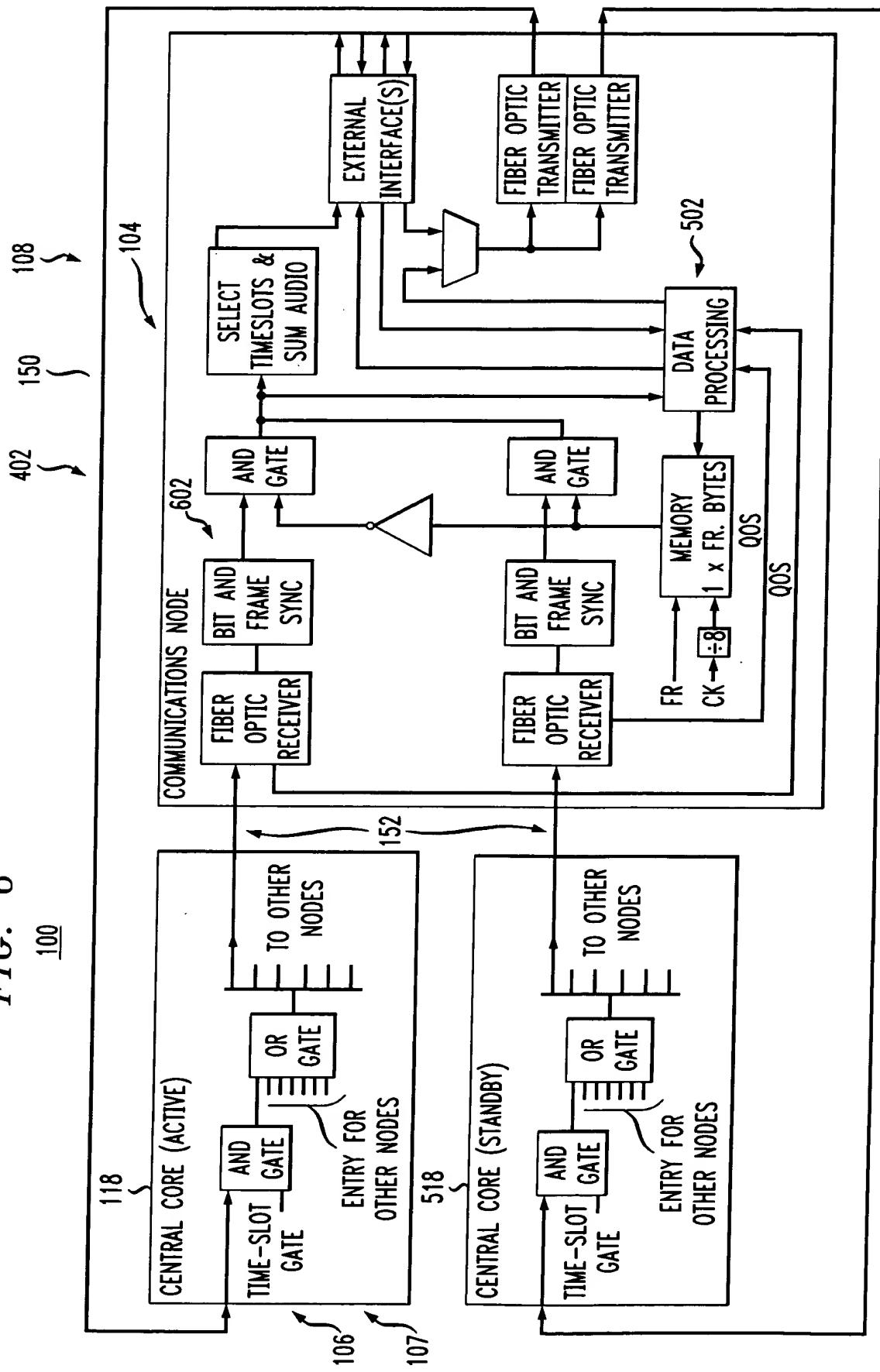
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FIG. 6



**FIG. 7**  
100

